



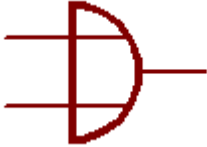

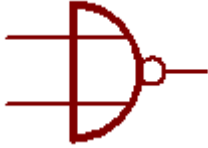

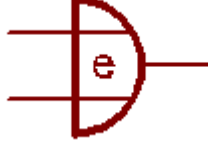

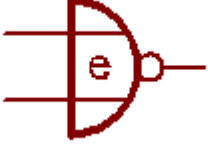

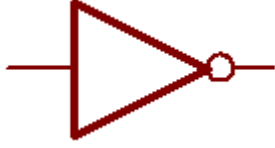
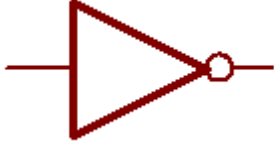


EU	US	Logiktable															
 AND		<table border="1"> <thead> <tr> <th>Input1</th> <th>Input2</th> <th>Output</th> </tr> </thead> <tbody> <tr><td>0</td><td>0</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>0</td></tr> <tr><td>1</td><td>0</td><td>0</td></tr> <tr><td>1</td><td>1</td><td>1</td></tr> </tbody> </table>	Input1	Input2	Output	0	0	0	0	1	0	1	0	0	1	1	1
Input1	Input2	Output															
0	0	0															
0	1	0															
1	0	0															
1	1	1															
 NAND		<table border="1"> <thead> <tr> <th>Input1</th> <th>Input2</th> <th>Output</th> </tr> </thead> <tbody> <tr><td>0</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>0</td></tr> </tbody> </table>	Input1	Input2	Output	0	0	1	0	1	1	1	0	1	1	1	0
Input1	Input2	Output															
0	0	1															
0	1	1															
1	0	1															
1	1	0															
 OR		<table border="1"> <thead> <tr> <th>Input1</th> <th>Input2</th> <th>Output</th> </tr> </thead> <tbody> <tr><td>0</td><td>0</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>1</td></tr> </tbody> </table>	Input1	Input2	Output	0	0	0	0	1	1	1	0	1	1	1	1
Input1	Input2	Output															
0	0	0															
0	1	1															
1	0	1															
1	1	1															
 NOR		<table border="1"> <thead> <tr> <th>Input1</th> <th>Input2</th> <th>Output</th> </tr> </thead> <tbody> <tr><td>0</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>0</td></tr> <tr><td>1</td><td>0</td><td>0</td></tr> <tr><td>1</td><td>1</td><td>0</td></tr> </tbody> </table>	Input1	Input2	Output	0	0	1	0	1	0	1	0	0	1	1	0
Input1	Input2	Output															
0	0	1															
0	1	0															
1	0	0															
1	1	0															
 Exklusiv OR		<table border="1"> <thead> <tr> <th>Input1</th> <th>Input2</th> <th>Output</th> </tr> </thead> <tbody> <tr><td>0</td><td>0</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>0</td></tr> </tbody> </table>	Input1	Input2	Output	0	0	0	0	1	1	1	0	1	1	1	0
Input1	Input2	Output															
0	0	0															
0	1	1															
1	0	1															
1	1	0															
 Exklusiv NOR		<table border="1"> <thead> <tr> <th>Input1</th> <th>Input2</th> <th>Output</th> </tr> </thead> <tbody> <tr><td>0</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>0</td></tr> <tr><td>1</td><td>0</td><td>0</td></tr> <tr><td>1</td><td>1</td><td>1</td></tr> </tbody> </table>	Input1	Input2	Output	0	0	1	0	1	0	1	0	0	1	1	1
Input1	Input2	Output															
0	0	1															
0	1	0															
1	0	0															
1	1	1															
 Inverter		<table border="1"> <thead> <tr> <th>Input1</th> <th>Output</th> </tr> </thead> <tbody> <tr><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td></tr> </tbody> </table>	Input1	Output	0	1	1	0									
Input1	Output																
0	1																
1	0																